

T1	T2	T3
READ(A)		
		READ(A)
	READ(A)	
	WRITE(A)	
WRITE(A)		
		READ(B)

Lost update / kein Inconsistent retrieval

T1	T2	T3
READ(A)		
		READ(A)
		READ(B)
	READ(A)	
	WRITE(A)	
WRITE(A)		

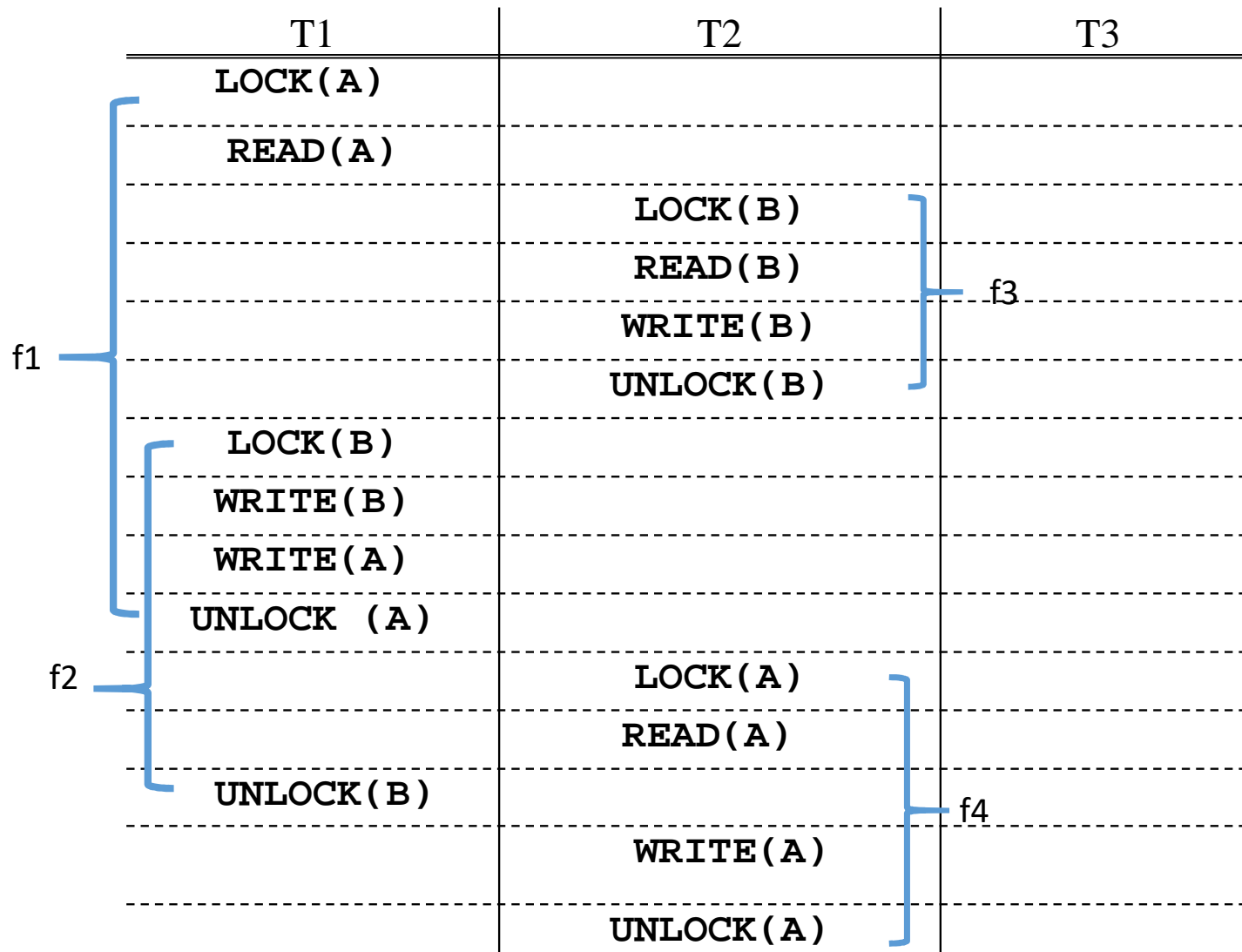
T1	T2	T3
		READ(A)
WRITE(A)		
	READ(A)	
	WRITE(B)	
		READ(B)

Inconsistent retrieval

T1	T2
READ(A)	
	READ(A)
	WRITE(A)
WRITE(A)	

... mit R/W Locks 2PC:

T1	T2
R-Lock(A)	
READ(A)	
	R-LOCK(A)
	READ(A)
	DL Abort
W-LOCK(A)	
WRITE(A)	
UNLOCK(A)	
	R-LOCK(A)
	READ(A)
	W-LOCK(A)
	WRITE(A)
	UNLOCK(A)



T1T2 => f4(f1(A)) und f3(f2(B))

T2T1 => f1(f4(A)) und f2(f3(B))

S => f4(f1(A)) und f2(f3(B))

➔ S ungleich T1T2 und S ungleich T2T1

➔ S ist nicht äquivalent zu einem
seriellen Schedule

➔ S ist nicht serialisierbar!

T1	T2	t(T1)	t(T2)	Do Nothing	Abort	t _r (A)	t _w (A)	t _r (B)	t _w (B)
		290	310			300	310	290	320
READ(A)					T1				
	READ(B)							310	
WRITE(A)									
	WRITE(B)			ja					

T1	T2	t(T1)	t(T2)	Do Nothing	Abort	t _r (A)	t _w (A)	t _r (B)	t _w (B)
		320	330			300	290	290	290
WRITE(A)							320		
	WRITE(B)								330
	READ(A)					330			
READ(B)					T1, T2				

T1	T2	T3	t(T1)	t(T2)	T(T3)	Abort	t _r (A)	t _w (A)	t _r (B)	t _w (B)
			320	310	300		300	290	290	290
WRITE(A)								320		
	WRITE(B)									310
	READ(A)					T2				
READ(B)									320	
		READ(B)				T3				